

Research Facility Development Division
Research Instruments Group
Data System Team

1. Abstract

This team is in charge of development, management and operation of the computing and network environment, mail and information services and data acquisition system and management of the information security of the RIKEN Nishina Center.

2. Major Research Subjects

- (1) Development, management and operation of the general computing servers
- (2) Development, management and operation of the mail and information servers
- (3) Development, management and operation of the data acquisition system
- (4) Development, management and operation of the network environment
- (5) Management of the information security

3. Summary of Research Activity

This team is in charge of development, management and operation of the computing and network environment, mail and information services and data acquisition system, and management of the information security of the RIKEN Nishina Center. The details are described elsewhere in this progress report.

(1) Development, management and operation of the general computing servers

We are operating Linux cluster system for the data analysis of the experiments and general computing. There are approximately 100 active user accounts on this cluster system. We are adopting the latest version of the Linux operating system. In addition to the cluster system, we have constructed analysis environment on the private cloud system of HOKUSAI Sailing Ship operated by Information Systems Division in RIKEN. These resources are dynamically allocated to several experimental projects.

(2) Development, management and operation of the mail and information servers

We are operating RIBF.RIKEN.JP server as a file and authentication server. This server is a core server of RIBF Linux cluster system. In addition, we are operating several information servers such as Web, Integrated Digital Conference (INDICO), Wiki, and Groupware servers.

(3) Development, management and operation of the data acquisition system

We have developed the standard data-acquisition system named as RIBFDAQ. This system can process up to 40 MB/s data. By using crate-parallel readout from front-end systems such as CAMAC and VME, the dead time could be minimized. To synchronize the independent DAQ systems, the time stamping system has been developed. The resolution and depth of the time stamp are 10 ns and 48 bits, respectively. This time stamping system is very useful for beta decay experiments such as EURICA, BRIKEN and VANDLE projects. One of the important tasks is the DAQ coupling, because detector systems with dedicated DAQ systems are transported to RIBF from other facilities. In case of SAMURAI Silicon (NSCL/TUM/WUSTL), the readout system is integrated into RIBFDAQ. The projects of MUST2 (GANIL), MINOS (CEA Saclay), NeuLAND (GSI) and TRB3 (TUM) cases, data from their DAQ systems are transferred to RIBFDAQ and merged online. For SPIRIT (RIKEN/GANIL/CEA Saclay/NSCL), RIBFDAQ is controlled from the NARVAL-GET system that is a large-scale signal processing system for the time projection chamber. EURICA (GSI), BRIKEN (GSI/Univ. Liverpool/IFIC), VANDLE (UTK), OTPC (U. Warsaw) and IDATEN (IBS/GSI/RIKEN) projects, we adopt the time stamping system to apply individual trigger for each detector system. In this case, data are merged in offline. In addition, we are developing intelligent circuits based on FPGA. General Trigger Operator (GTO) is an intelligent triggering NIM module. Recently an upgrade version of GTO has been developed and named as GTO revolution (GTR). LUPO is an FPGA-based logical I/O module that has been used mainly for time stamping applications. Its successor, CLIO, was developed in 2024. To improve the data readout speed of VME system, we have successfully developed the MPV system which is a parallel readout extension of the VME system. Thanks to the MPV system, now the DAQ system in RIBF is 20 times faster than in 2007. Toward to the next generation DAQ system, we have started to develop a real-time data processing unit based on AMD RFSoc that includes FPGA and 4 GHz FADC for TOF measurements. For gaseous detectors like PPAC and drift chamber, the development of FPGA-based dead-time free TDC is now in progress. For Silicon semiconductor detectors, waveform digitizer based front-end electronics is commissioned. From 2022, Signal processing and data acquisition infrastructure (SPADI) alliance is launched to advance collaborative DAQ development in Japanese nuclear physics community. This team participates as one of the core members of the SPADI alliance.

(4) Development, management and operation of the network environment

We have been managing the network environment collaborating with Information Systems Division in RIKEN. All the Ethernet ports of the information wall sockets are capable of the Gigabit Ethernet connection (10/100/1000 bps). In addition, several 10 Gbps network ports have been introduced to RIBF experimental area. We have been operating approximately 90 units of wireless LAN access points in RNC. Almost the entire radiation-controlled area of the East Area of RIKEN Wako campus is covered by wireless LAN for the convenience of experiments and daily work. In 2022, wireless LAN system (including Guest and Eduroam network) operated by RIKEN Information Systems Division is additionally introduced in the measurement rooms in RIBF B1F and B3F.

(5) Management of the information security

It is essential to take proper information security measures for information assets. We are managing the information security of Nishina Center collaborating with Information Systems Division in RIKEN.

Members**Team Leader**

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Marina ARAI

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List of Presentations**Presentations****[International Conferences/Workshops]**

Y. Ichinohe, "High-speed data processing in the RIBF DAQ system using the Alveo data-center accelerator card," 24th IEEE Real Time Conference, Quy Nhon, Vietnam, April 22–26, 2024.

S. Takeshige, "Development of a high-bandwidth waveform processing system using RFSoc for RI beam experiments," 24th IEEE Real Time Conference, Quy Nhon, Vietnam, April 22–26, 2024.

H. Baba, "Clock timing synchronization module with AMD Kria SOM," Streaming Readout Workshop SRO-XII, Tokyo, Japan, December 2–4, 2024.

Y. Ichinohe, "High-speed data processing in the RIBF DAQ system using Alveo/Versal data-center accelerator cards," Streaming Readout Workshop SRO-XII, Tokyo, Japan, December 2–4, 2024.

[Domestic Conferences/Workshops]

馬場秀忠 (口頭発表), 「Clock Sync Module」, The 2nd Workshop on Signal processing and data acquisition infrastructure (SPADI2024), 和光市 (理化学研究所), 2024 年 3 月 8 日.

馬場秀忠 (口頭発表), 「MIRA デジタイザの開発」, 日本物理学会第 79 回年次大会, 札幌 (北海道大学), 2024 年 9 月 16–19 日.

武重祥子 (口頭発表), 「不安定核弾性散乱測定のための検出器アレイ DELTA の開発」, 日本物理学会第 79 回年次大会, 札幌 (北海道大学), 2024 年 9 月 16–19 日.